

1. Release Notes for Current Version

Firmware Name	5001NOR001002.ima
Release Version	00.10
Release Date	2025/12/05
Codebase	AST2600 LTS v13.7
Change log	1. [Feature] Modified power on delay time per EE request.
Known Issue	
Important Notes	Must be used with BIOS version Nora1050/Norm0080 or above. Must be used with CPLD version v0.0.7 or above.

2. Release Notes for Previous Version

Firmware Name	5001NOR000902.ima
Release Version	00.09
Release Date	2025/12/03
Codebase	AST2600 LTS v13.7
Change log	2. [Feature] Updated SDR list. 3. [Feature] Update NVMe temperature threshold from thermal. 4. [Feature] Change power cycle delay time.
Known Issue	
Important Notes	Must be used with BIOS version Nora1050/Norm0080 or above. Must be used with CPLD version v0.0.6 or above.

3. Release Notes for Previous Version

Firmware Name	5001NOR000802.ima
Release Version	00.08
Release Date	2025/11/25
Codebase	AST2600 LTS v13.7
Change log	5. [Feature] Updated SDR list. 6. [Feature] Updated fan table from thermal team. 7. [Feature] Support ADDC function. 8. [Fix][#10370] After the Operational temp test,the NVMe _TEMP Upper critical going high Reading 55>Threshold 55 degrees C 9. [Fix][#10369] After the Operational temp test,the Front and Rear side1/2 Temperature Upper critical going high Reading 50>Threshold 50 degrees C 10. [Fix][#10328] Cannot identify the sensor event when "PX_CX_VR_Temp" triggers a thermal shutdown.
Known Issue	
Important Notes	Must be used with BIOS version Nora1050/Norm0080 or above.

4. Release Notes for Previous Version

Firmware Name	5001NOR000702.ima
Release Version	00.07
Release Date	2025/11/05
Codebase	AST2600 LTS v13.7
Change log	11. [Feature] Support GPU9 temperature sensor. 12. [Feature] Support PSU5 and PSU6 status and temperature sensor. 13. [Feature] Implement NVMe LED control command. 14. [Fix][#9332] SNMP v1 "add-in card" event raw data is displayed but cannot be identified. 15. [Fix] Correct I2C switch slave address for GPU5-8 temperature reading on DVT system. 16. [Fix] AMD DIMM temperature decoding for negative values. 17. [Fix] System Inventory baseboard IPv6 Addresses Info is empty. 18. [Fix] Fan readings were incorrect when NCT7904D read operations failed.
Known Issue	
Important Notes	Must be used with BIOS version Nora1050/Norm0080 or above.

5. Release Notes for Previous Version

Firmware Name	5001NOR000602.ima
Release Version	00.06
Release Date	2025/10/23
Codebase	AST2600 LTS v13.7
Change log	19. [Fix] GPU 12V is dropped in PDB without efuse. 20. [Feature] Support VR throttled CPU function. 21. [Feature] Updated VR temperature sensor UC and UNR from VR chip. 22. [Feature] Support BIOS 2nd ROM FW update. 23. [Feature] Support access DIMM via I3C bus.
Known Issue	
Important Notes	Must be used with BIOS version Nora1050/Norm0080 or above.

6. Release Notes for Previous Version

Firmware Name	5001NOR000502.ima
Release Version	00.05
Release Date	2025/10/08
Codebase	AST2600 LTS v13.7
Change log	24. [Feature] Support OEM command for NVMe LED control. 25. [Feature] Update fan table from thermal team.
Known Issue	
Important Notes	Must be used with BIOS version Nora1050/Norm0080 or above.

7. Release Notes for Previous Version

Firmware Name	5001NOR000402.ima
Release Version	00.04
Release Date	2025/09/26
Codebase	AST2600 LTS v13.7
Change log	1. [Feature] Updated SDR list. 2. [Feature] Support CPU, DIMM, NVMe temperature sensor. 3. [Fix][#9371] The Memory ECC error event cannot be mapped to a physical slot in the SEL.
Known Issue	
Important Notes	Must be used with BIOS version Nora1020/Norm0050 or above.

8. Release Notes for Previous Version

Firmware Name	5001NOR000302.ima
Release Version	00.03
Release Date	2025/08/15
Codebase	AST2600 LTS v13.7
Change log	1. [Feature] Updated SDR list. 2. [Feature] Support system inventory. 3. [Fix][#9423]The displayed name of the "PSU_Total_Watt" sensor does not match the SDR file. 4. [Fix][#9273] There is no "CPU1_Status" sensor information shown in the SDR. 5. [Feature] Show 2nd version information of BIOS image during BIOS FW update by web. 6. [Feature] Improved latency of battery sensor monitoring. 7. [Feature] Initialize PDB board to protect over current.
Known Issue	No support CPU temperature reading yet. No support DIMM temperature reading yet.
Important Notes	Must be used with BIOS version Nora1020/Norm0050 .

9. Release Notes for Previous Version

Firmware Name	5001NOR000202.ima
Release Version	00.02
Release Date	2025/05/08
Codebase	AST2600 LTS v13.7
Change log	1. [Feature] Updated SDR list. 2. [Fix][#9371] The Memory ECC error event cannot be mapped to a physical slot in the SEL. 3. [Fix][#9257] The sensor of "Voltage P0_VDD_33_DUAL" asserted Lower Critical going low during AC cycle test. 4. [Fix][#9293] Active Heatsink CPU1_fan speed can not be controlled. 5. [Fix][#9074] [SI][Norma_A01][SKU:A] UEFI Shell cannot detect the USB disk. 6. [Fix][#9232] The sensor of "Voltage P0_VDD_18" asserted Upper Critical going high during stressapptest. 7. [Fix][#9284] The fan redundancy feature is not working. 8. [Fix][#9242] The fan speed does not increase when the GPU temperature rises.
Known Issue	No support CPU temperature reading yet. No support DIMM temperature reading yet.
Important Notes	None

10.Release Notes for Previous Version

Firmware Name	5001NOR000102.ima
Release Version	00.01
Release Date	2025/04/18
Codebase	AST2600 LTS v13.7
Change log	First Release
Known Issue	No support CPU temperature reading yet. No support DIMM temperature reading yet.
Important Notes	None