

BIOS Release Notes

TITLE

SUBJECT: MZ32-AR0 BIOS Release Notes version R14_F02
System: R152-Z30-00, R152-Z31-00, R152-Z32-00, R272-Z30-00, R272-Z31-00,
R272-Z32-00, W282-001-JN, R272-Z33-FL, S452-Z30-00

About This Release

Build Date: 2020/02/25
BIOS Checksum: 0x1AAF04B97 (32M)
Release Owner: Hex.Jan

BIOS Components/Contents

Processor stepping(s) supported: AMD EPYC Rome Processors
System hardware configurations supported: MZ32-AR0, MZ32-AR1
Microcode updates versions:

CPUID	Family	Microcode Update ID
Rome A0	Family 17h Model 30-3Fh	08300027
Rome B0	Family 17h Model 30-3Fh	08301034

IPMI support:
AMI Kernel version: 5.14_RomeCrb_0ACMK014
AGESA PI version: RomePI 1.0.0.6
OPROM version :

OPROM	Version
AST2500	1.09.0

Installation Notes

IMPORTANT NOTES:

1. Please extract the MZ32-AR0_R14_F02.zip to a bootable diskette that use FAT/FAT32 format

BIOS UPDATE INSTRUCTIONS FOR EFI Shell:

1. Insert USB flash drive to system for BIOS upgrade.
2. Power on system and boot to Build-In Shell.
3. Enter your USB filesystem, like "fs0:" or "fsx:", "x" is your USB filesystem number
4. Execute F.nsh for bios update
5. After bios flash finish, please reboot system

BIOS UPDATE INSTRUCTIONS FOR Windows:

1. Insert bios update USB flash drive.
2. Use Command Shell

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3. Execute \Tool\WIN\Win32*.bat or \Tool\Win64*.bat for bios update
4. After bios flash finish, please reboot system

BIOS Version CHECK INSTRUCTIONS:

1. Power on system and press during POST
2. The bios version shows on the first main page

Known Issues/Workarounds

1. Windows OS does not support 256 or more logical.
Windows 2019 can update KB4512534 to support it.

item	OS	Win2019	Win2016
IOMMU		Enable ※	Enable KB4022723
SMT Control		Enable KB4512534	Disable

※When SMT Enabled, IOMMU need to set Enabled too.

SMT Control: AMD Simultaneous Multi-Threading Technology

KB4022723: Support IOMMU on Windows 2016.

KB4512534: Support SMT on Windows 2019.

2. WS2019 Hyper-V support: Add Microsoft patch KB4512534.

Issues fixed in this version

- 1 . Naples CPU please reference RelNotes_Naples.PDF
- 2 . Rome CPU please reference RelNotes_Rome.PDF