ReleaseOrder ID:	*SCGCQ01445309*
Headline:	*Point Release: SAS3 IT Legacy x86 Option ROM: Ver*
Release Version:	*8.35.04.00*
UCM Project:	*BIOS_MPT_GEN3_Phase15.0*
UCM Stream:	*BIOS_MPT_GEN3_Phase15.0_GenPoint*
Release Type:	*Point*
State:	*Deployed*
Release Baseline:	*BIOS_MPT_GEN3_Phase15.0_GenPoint_2017-09-08@ \SAS2*
Release Date:	*13-SEP-17*
Date Generated:	*Nov 13, 2017*

Defects Fixed (1):

ID: SCGCQ01379363

Headline: OpROM POST display is incorrect when at least one HBA is disabled in configuration utility.

Description Of Change: OpROM displays POST information based on saved configurations. These configurations are read and updated if needed on every reboot. The settings were read from a HBA but were being written into first HBA that was detected instead of where it was read from. Change is to read and write into same HBA.

Issue Description: OpROM POST display contains disabled HBA information. Some HBA are disabled in configuration utility and PCI slots where these HBA are connected are disabled to load legacy OpROM. There is at least one HBA which is enabled and in boot order. These HBA are moved from other system. OpROM is expected to reconfigure the settings in new system on first boot and subsequent reboots should display correct information in POST.

Steps To Reproduce:
1. Connect 3 HBA to a system.
2. Disable 2 of them and keep one enabled. Make sure all 3 HBA are in boot order.
3. Save the changes.
4. Migrate all 3 HBA to another system in which PCI Slots can be enabled/disabled from loading legacy OpROM.
5. Disabled PCI Slots of HBA which were disabled in step 2.
6. Reboot system and observe that OpROM POST displays disabled HBA information in every reboots.