ReleaseOrder ID:	*SCGCQ01221590*
Headline:	*GCA: BIOS_MPT_GEN3_Phase14.0 - 8.33.00.00 BIOS*
Release Version:	*8.33.00.00*
UCM Project:	*BIOS_MPT_GEN3_Phase14.0*
UCM Stream:	*BIOS_MPT_GEN3_Phase14.0_Main_Dev*
Release Type:	*GCA*
State:	*Deployed*
Release Baseline:	*BIOS_MPT_GEN3_Phase14.0_Main_Dev_2016-11-17@ \SAS2*
Release Date:	*20-DEC-16*
Date Generated:	*Jan 24, 2017*

Release History

- SCGCQ01215378 - Beta Release: BIOS_MPT_GEN3_Phase14.0 - 8.32.02.0

- SCGCQ01177135 - Beta Release: BIOS_MPT_GEN3_Phase14.0 - 8.32.01.0

ReleaseOrder ID:	*SCGCQ01215378* Open In CQWeb
Headline:	*Beta Release: BIOS_MPT_GEN3_Phase14.0 - 8.32.02.0*
Release Version:	*8.32.02.00*
UCM Project:	*BIOS_MPT_GEN3_Phase14.0*
UCM Stream:	*BIOS_MPT_GEN3_Phase14.0_Main_Dev*
Release Type:	*Beta*
State:	*Superseded*
Release Baseline:	*BIOS_MPT_GEN3_Phase14.0_Main_Dev_2016-11-07@ \SAS2*
Release Date:	*17-NOV-16*
Date Generated:	*Jan 24, 2017*

Defects Fixed (1):

ID:	SCGCQ01206764 (Port Of Defect SCGCQ01191180)
Headline:	Connecting a Faulty drive to the controller results in BIOS CU crash
Description Of Change:	The faulty drive requires the BIOS CU to send Start Stop Unit command. In the Phase-13 version of BIOS CU before sending the SSU command an Inquiry command is sent to identify the type of the device however the Inquiry data buffer was unallocated which results in the crash. The change is to allocate proper memory for Inquiry buffer which resolve the CU crash.
Issue Description:	When a faulty drive is connected to SAS3 controller and BIOS CU is invoked it is stuck at "reading no-volatile settings".
Steps To Reproduce:	1. Connect this fault drive to SAS3 HBA 2. Power on the system and press ctrl+c to invoke CU 3. Select the adapter and press "enter" 4. Observe BIOS CU stuck at "Reading non-volatile settings..."

ReleaseOrder ID:	*SCGCQ01177135* Open In CQWeb
Headline:	*Beta Release: BIOS_MPT_GEN3_Phase14.0 - 8.32.01.0*
Release Version:	*8.32.01.00*
UCM Project:	*BIOS_MPT_GEN3_Phase14.0*
UCM Stream:	*BIOS_MPT_GEN3_Phase14.0_Main_Dev*
Release Type:	*Beta*
State:	*Superseded*
Release Baseline:	*BIOS_MPT_GEN3_Phase14.0_Main_Dev_2016-09-08@ \SAS2*
Release Date:	*12-SEP-16*
Date Generated:	*Jan 24, 2017*

Enhancements Implemented (1):

ID:	SCGCQ01167765
Headline:	Move PCI Bus Master Enabling and Latency Timer settings to early HBA initialization time.
Description Of Change:	The PCI Bus Master enabling and Latency Timer settings are executed at a later stage in HBA initialization time during core BIOS initialization. However it is possible that some of the PCI transactions could have been attempted and might have failed prior to enabling of Bus Master. Hence the enabling of bus mastering is moved to be executed immediately after HBA is detected.