



SCS Engineering Release Notice

Phase8 GCA Release Version 8.17.00.00 - BIOS_MPT_GEN3_Phase8.0 (SCGCQ00822259)

(SCGCQ00822259) - Phase8 GCA Release Version 8.17.00.00 - BIOS MPT GEN3 Phase8.0

(SCGCQ00807504) - Phase8 Alpha Release Version 8.16.03.00 - BIOS MPT GEN3 Phase8.0

(SCGCQ00796078) - Phase8 Pre-Alpha Release Version 8.16.01.00 -

BIOS MPT GEN3 Phase8.0



SCS Engineering Release Notice

Phase8 GCA Release Version 8.17.00.00 - BIOS_MPT_GEN3_Phase8.0 (SCGCQ00822259)

Defects=0, Enhancements=0 (Version Change Only)



SCS Engineering Release Notice

Phase8 Alpha Release Version 8.16.03.00 - BIOS_MPT_GEN3_Phase8.0 (SCGCQ00807504)

Change Summary (Defects=1)

SCGCQ00801767 (DFCT) - INT13 Function 41 reporting 64 bit support in error



SCS Engineering Release Notice

Phase8 Alpha Release Version 8.16.03.00 - BIOS_MPT_GEN3_Phase8.0 (SCGCQ00807504)

Total Defects Resolved (1)

(SCGCQ00801767)	Defect 1/1
HEADLINE:	INT13 Function 41 reporting 64 bit support in error
DESC OF CHANGE:	Bit 3, returned in register CS for Function 41 is now returned off.
TO REPRODUCE:	Attempt to write into high memory using Data Packet DWord at offset 4 with a value of FFFFFFFF and appropriate 64 bit address settings. It will fail. The Phase 8 BIOS will now reject a request to access memory addresses in excess of 32 bits.
ISSUE DESC:	BIOS incorrectly indicated we were supporting 64 bit memory addressing in Legacy BIOS. Following a reassessment of the EDD 4.0 spec, and its meaning regarding 64 bit memory support, we determined that the original implementation was wrong, and we are resetting the bit that said we were providing support. This is INT13h Function 41, CX register bit 3.



SCS Engineering Release Notice

Phase8 Pre-Alpha Release Version 8.16.01.00 - BIOS_MPT_GEN3_Phase8.0 (SCGCQ00796078)

Change Summary (Enhancements=1)

SCGCQ00795647 (CSET) - Add capability to change the Port Configuration in the BIOS Configuration Utility (CU)



SCS Engineering Release Notice

Phase8 Pre-Alpha Release Version 8.16.01.00 - BIOS_MPT_GEN3_Phase8.0 (SCGCQ00796078)

Total Enhancements Implemented (1)

(SCGCQ00795647 - Port of SCGCQ00742008)

Enhancement 1/1

HEADLINE:

Add capability to change the Port Configuration in the BIOS Configuration Utility (CU)

NEW FUNCTIONALITY:

Created a new "Advanced Port Properties" screen with displays of the Current Port Properties and Configuration, with the ability to select a New Port Configuration. This feature is enabled by default in channel HBAs only. The OEM specific NVDATA is not modified to enable this feature.