

**ReleaseOrder ID:** SCGCQ01558801  
**Headline:** SAS3.5(Ventura) IT Legacy x86 Option ROM: RC vers  
**Release Version:** 9.11.00.00  
**UCM Project:** BIOS\_MPT\_GEN3.5\_Phase6.0  
**UCM Stream:** BIOS\_MPT\_GEN3.5\_Phase6.0\_Rel  
**Release Type:** ReleaseCandidate  
**State:** Deployed  
**Release Baseline:** BIOS\_MPT\_GEN3.5\_Phase6.0\_Rel\_2018-01-11@|SAS35  
**Release Date:** 16-JAN-18  
**Date Generated:** Feb 16, 2018

## Release History

- [SCGCQ01479383 - SAS3.5\(Ventura\) IT Legacy x86 Option ROM: Beta ve](#)
- [SCGCQ01459258 - SAS3.5\(Ventura\) IT Legacy x86 Option ROM: Pre-Alp](#)

**ReleaseOrder ID:** [SCGCQ01479383](#) [Open In CQWeb](#)  
**Headline:** SAS3.5(Ventura) IT Legacy x86 Option ROM: Beta ve  
**Release Version:** 9.10.02.00  
**UCM Project:** BIOS\_MPT\_GEN3.5\_Phase6.0  
**UCM Stream:** BIOS\_MPT\_GEN3.5\_Phase6.0\_Rel  
**Release Type:** Beta  
**State:** In\_Review  
**Release Baseline:** BIOS\_MPT\_GEN3.5\_Phase6.0\_Rel\_2017-10-17@|SAS35  
**Release Date:** 05-DEC-17  
**Date Generated:** Feb 16, 2018

### Defects Fixed (1):

**ID:** SCGCQ01465443 (Port Of Defect SCGCQ01460405)  
**Headline:** BIOS POST displays duplicate entries of an HBA.  
**Description Of Change:** During initialization, BIOS populates all the adapters that are detected and later evaluates each adapter's configuration so that they will be removed or kept in the list based on their configurations. OpROM correctly identified the adapters that are to be removed but did not erase their entry which resulted in displaying wrong adapters and sometimes a duplicate entry. Fix is to erase the adapter entry which has to be removed from the list.  
**Issue Description:** OpROM Post displays disabled adapters. The adapters are disabled in BIOS configuration utility. In some specific systems with specific system settings, there are instances where a duplicate adapter entry is displayed in OpROM POST.  
**Steps To Reproduce:** Connect at least 3 controllers to a system. Disable some of them in CU. Observe in POST that disabled controllers are listed. Some time duplicate entry is observed.

**ReleaseOrder ID:** [SCGCQ01459258](#) [Open In CQWeb](#)  
**Headline:** SAS3.5(Ventura) IT Legacy x86 Option ROM: Pre-Alp  
**Release Version:** 9.10.01.00  
**UCM Project:** BIOS\_MPT\_GEN3.5\_Phase6.0  
**UCM Stream:** BIOS\_MPT\_GEN3.5\_Phase6.0\_Rel  
**Release Type:** Pre-Alpha  
**State:** In\_Review  
**Release Baseline:** BIOS\_MPT\_GEN3.5\_Phase6.0\_Rel\_2017-09-26@|SAS35  
**Release Date:** 12-DEC-17  
**Date Generated:** Feb 16, 2018

### Defects Fixed (2):

**ID:** SCGCQ01445431 (Port Of Defect SCGCQ01379363)  
**Headline:** OpROM POST display is incorrect when at least one HBA is disabled in configuration utility.  
**Description Of Change:** OpROM displays POST information based on saved configurations. These configurations are read and updated if needed on every reboot. The settings were read from an HBA but were being written into first HBA that was detected instead of where it was read from. Change is to read and write into same HBA.  
**Issue Description:** OpROM POST display contains disabled HBA information. Some HBA is disabled in configuration utility and PCI slots where these HBA are connected are disabled to load legacy OpROM. There is at least one HBA which is enabled and in the boot order. These HBA are moved from another system. OpROM is expected to reconfigure the settings in the new system on first boot and subsequent reboots should display correct information in POST.  
**Steps To Reproduce:**

1. Connect 3 HBA to a system.
2. Disable 2 of them and keep one enabled. Make sure all 3 HBA are in the boot order.
3. Save the changes.
4. Migrate all 3 HBA to another system in which PCI Slots can be enabled/disabled from loading legacy OpROM.
5. Disabled PCI Slots of HBA which were disabled in step 2.
6. Reboot the system and observe that OpROM POST displays disabled HBA information in every reboot.

**ID:** SCGCQ01458176 (Port Of Defect SCGCQ01271640)  
**Headline:** Boot device is displayed twice when Int 13h set to 0.  
**Description Of Change:** When an user has selected max int 13 disks as zero, Int 13 chain will be empty. However, a user can select to display all devices in POST using global settings. BIOS always checks and adds boot devices that are supposed to be added top of the chain. Since there will not be a chain BIOS made to ignore any boot devices and continue with disk display as they are discovered.  
**Issue Description:** Boot device is displayed twice when Int 13h set to 0.  
**Steps To Reproduce:** Flash the latest legacy OpROM and Firmware.  
 Connect few drives and enter into BIOS CU.  
 Enter into Global Properties.  
 Set the values as below:  
 Pause when Boot Alert Displayed - [Yes]  
 Boot Information Display Mode - [Display Adapters and All Devices]  
 Select a drive under SAS Topology and set to Boot by pressing Alt B.  
 Enter into Advance Adapter Properties and set the Int 13h to 0.  
 Save the settings and Reboot, observe the Post.  
 The boot device is displayed twice.